### Notification about the transfer of the semiconductor business

The semiconductor business of Panasonic Corporation was transferred on September 1, 2020 to Nuvoton Technology Corporation (hereinafter referred to as "Nuvoton"). Accordingly, Panasonic Semiconductor Solutions Co., Ltd. became under the umbrella of the Nuvoton Group, with the new name of Nuvoton Technology Corporation Japan (hereinafter referred to as "NTCJ").

In accordance with this transfer, semiconductor products will be handled as NTCJ-made products after September 1, 2020. However, such products will be continuously sold through Panasonic Corporation.

Publisher of this Document is NTCJ.

If you would find description "Panasonic" or "Panasonic semiconductor solutions", please replace it with NTCJ.

Except below description page
 "Request for your special attention and precautions in using the technical information and semiconductors described in this book"

Nuvoton Technology Corporation Japan

Doc No. TT4-EA-14513

Revision. 2

MOS FET

FC6B22090L

# **Panasonic**

## FC6B22090L

### Gate resistor installed Dual N-channel MOS FET

For lithium-ion secondary battery protection circuits

#### ■ Features

- Low source-source ON resistance:Rss(on) typ. = 8.5 mΩ(VGS = 4.5 V)
- · CSP package:smallest & thinnest size
- · RoHS compliant (EU RoHS / MSL:Level 1 compliant)
- Marking Symbol: 32

#### ■ Packaging

Embossed type (Thermo-compression sealing): 10 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

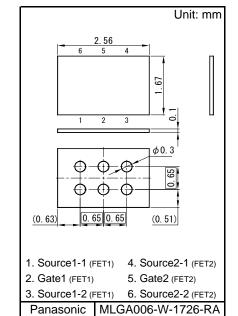
Parameter	Symbol	Rating	Unit
Source-source Voltage	VSS	24	V
Gate-source Voltage	VGS	±12	V
Source Current (DC) *1	IS	12	Α
Source Current (Pulsed) *2	ISp	60	Α
Total Power Dissipation *1	PD	2.1	W
Channel Temperature	Tch	150	°C
Storage Temperature Range	Tstg	-55 to +150	°C
Thermal Resistance (ch-a)	Rth(ch-a)	59	°C/W

Note \*1 Mounted on ceramic substrate (70 mm × 70 mm × t1.0 mm).

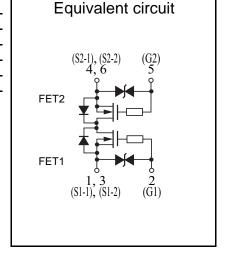
Established: 2013-01-28

: 2013-10-08

Revised



JEITA Code



<sup>\*2</sup>  $t = 10 \mu s$ , Duty Cycle  $\leq 1 \%$ 

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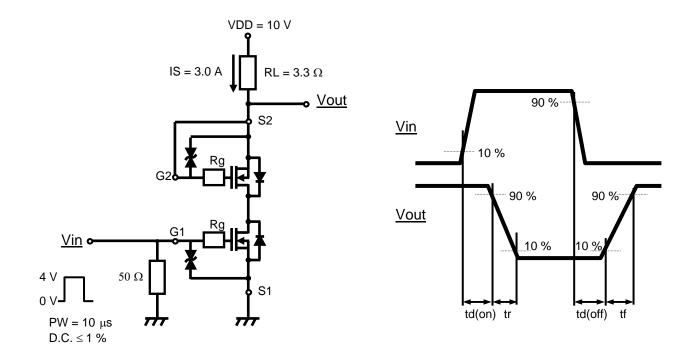
MOS FET FC6B22090L

#### ■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Source-source Breakdown Voltage	VSSS	IS = 1 mA, VGS = 0 V	24			V
Zero Gate Voltage Source Current	ISSS	VSS = 24 V, VGS = 0 V			1.0	μΑ
Gate-source Leakage Current	IGSS	$VGS = \pm 8 \text{ V}, VSS = 0 \text{ V}$			±10	μА
	1000	$VGS = \pm 5 V$ , $VSS = 0 V$			±1.0	
Gate-source Threshold Voltage	Vth	IS = 1.2 mA, VSS = 10 V	0.40	0.90	1.4	V
	RSS(on)1	IS = 3.0 A, VGS = 4.5 V	6	8.5	11.5	
Source-source On-state Resistance	RSS(on)2	IS = 3.0 A, VGS = 3.8 V	6.2	9	12.5	mΩ
	RSS(on)3	IS = 3.0 A, VGS = 3.1 V	6.7	10	15	
	RSS(on)4	IS = 3.0 A, VGS = 2.5 V	7.5	13	23	
Body Diode Forward Voltage	VF(s-s)	IF = 6.0 A, VGS = 0 V		8.0	1.2	V
Input Capacitance *1	Ciss			3000		
Output Capacitance *1	Coss	VSS = 10 V, VGS = 0 V, f = 1 MHz		250		pF
Reverse Transfer Capacitance *1	Crss			220		İ
Turn-on delay Time *1,*2	td(on)	VDD = 10 V, VGS = 0 to 4.0 V		1.1		0
Rise Time *1,*2	tr	IS = 3.0 A		1.8		μS
Turn-off delay Time *1,*2	td(off)	VDD = 10 V, VGS = 4.0 to 0 V		6.9		0
Fall Time *1,*2	tf	IS = 3.0 A		3.6		μS
Total Gate Charge *1	Qg	VDD = 10 V		27.6		
Gate-source Charge *1	Qgs	VGS = 0 to 4.0 V,		5.6		nC
Gate-drain Charge *1	Qgd	IS = 6.0 A		7.5		

Note Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

<sup>\*2</sup> Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time



Page 2 of 5

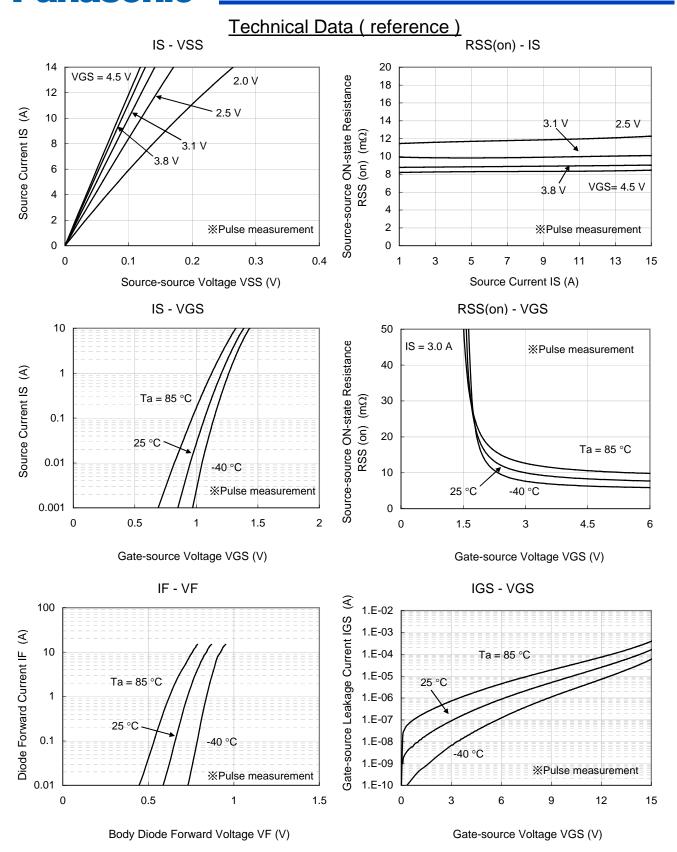
Established: 2013-01-28 Revised: 2013-10-08

<sup>\*1</sup> Assured by design

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MOS FET

FC6B22090L



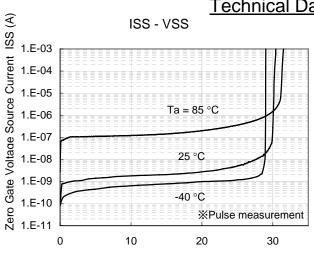
Page 3 of 5

Established: 2013-01-28 Revised: 2013-10-08 **Panasonic** 

MOS FET

FC6B22090L

# Technical Data (reference)



Parameter	Conditions	Result	
Farameter	Conditions	Kesuit	
Operation Test *1	VGS = 3.8  V, IS = 30  A,	PASS	
	t = 3 ms		
	VGS = 3.8 V, IS = 15 A,	PASS	
	t = 11 ms		
Destruction Current *1	VGS = 3.8 V,	100 A	
	t = 3 ms		
	VGS = 3.8 V,	68 A	
	t = 11 ms		

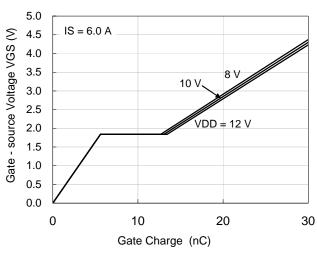
**Destruction Current** 

Ta = 25 °C,

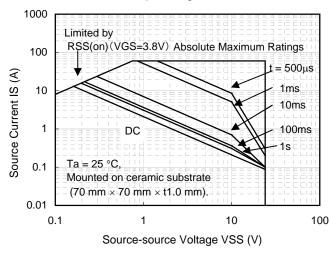
Mounted on FR4 board (  $25.4~mm \times 25.4~mm \times t1.0~mm$  ) using the minimum recommended pad size (  $36\mu m$  Copper ).

### **Dynamic Input/Output Characteristics**

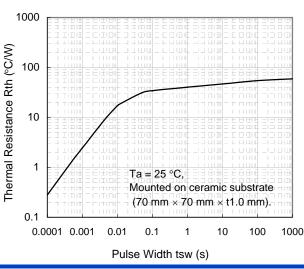
Source-source Voltage VSS (V)



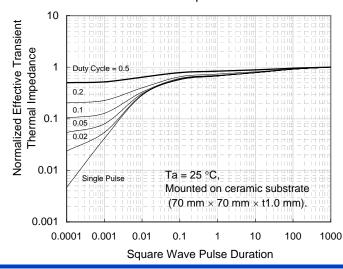
### Safe Operating Area



### Rth - tsw



#### Thermal Response



Page 4 of 5

Established: 2013-01-28 Revised: 2013-10-08 Doc No. TT4-EA-14513 Revision. 2

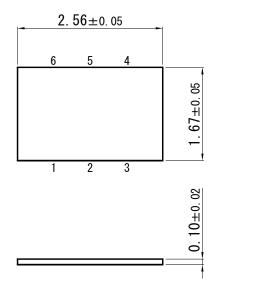
**Panasonic** 

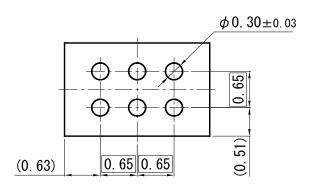
MOS FET

FC6B22090L

# MLGA006-W-1726-RA

Unit: mm

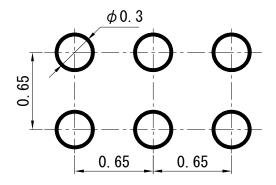




### ■ Land Pattern (Reference)

Established: 2013-01-28 Revised: 2013-10-08

Unit: mm



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